

**Department of Electronics & Communication Engineering,
Jaypee University of Information Technology**
in collaboration with

**Electronics & ICT Academy, Centre for Development of Advanced Computing
(C-DAC), Mohali, IEEE R10 EAC, IEEE India Council, IEEE Delhi Section,
IEEE MDC, SAC and WIE AG Delhi Section**

Organizing

One Week FACULTY DEVELOPMENT PROGRAM
VLSI DESIGN: BRIDGING THEORY AND PRACTICE
JUNE 09 - 13, 2025



Chief Guest

Mr. V.K. Sharma
Scientist-G, Director
CDAC Mohali



**Guest of Eminence
& Resource Person**

Dr. Balwinder Singh
Scientist-E, CI, E&ICT Academy
CDAC Mohali



**Guest of Honor
& Resource Person**

Prof. Manoj Saxena
Department of Electronics
University of Delhi



Resource Person

Prof. Rajendra Singh
Department of Physics
IIT Delhi



Resource Person

Prof. Sudeb Dasgupta
Department of ECE
IIT Roorkee



Resource Person

Mr. Harpreet S. Jatana
Former Group Head
SCL Mohali and ISRO