



JAYPEE UNIVERSITY OF INFORMATION TECHNOLOGY

WAKNAGHAT, P.O. – WAKNAGHAT, TEHSIL – KANDAGHAT, DISTRICT – SOLAN (H.P.) PIN – 173234 (INDIA) Phone Number- +91-1792-257999 (Established by H.P. State Legislature vide Act No. 14 of 2002)



Department of Electronics and Communication Engineering

Report on the Workshop:

Connecting Students to the Semiconductor Industry

Date: 1st May 2025 **Time:** 10:30 AM – 4:00 PM

The Department of Electronics and Communication Engineering (ECE), Jaypee University of Information Technology (JUIT), Waknaghat, organized an insightful workshop titled "Connecting Students to the Semiconductor Industry" on 1st May 2025. The session focused on Very-Large-Scale Integration (VLSI) technology and its growing career opportunities, featuring Mr. Rajat Kumar Singh, CEO of VLSI for All Pvt. Ltd., as the distinguished speaker.

The workshop brought together enthusiastic participation from B.Tech., M.Tech. and PhD students as well as faculty members, aiming to provide a comprehensive overview of the global VLSI landscape while motivating students to explore diverse career pathways in this rapidly evolving domain

Inauguration and Felicitation

The event began with a lamp-lighting ceremony, led jointly by Prof. Rajiv Kumar (HoD, ECE) and Mr. Rajat Kumar Singh. During the inauguration, Prof. Shruti Jain (Associate Dean – Innovation) felicitated Mr. Singh in recognition of his contributions to industry and academia.

The program was graced by senior faculty members including Dr. Harsh Sohal, Dr. Emjee Puthooran, Dr. Naveen Jaglan, Dr. Shweta Pandit and others along with the active participation of ECE students who played a key role in organizing and managing the workshop.

Key Highlights of the Session

• **Industry Insights:** Mr. Singh presented a detailed overview of VLSI technology and its pivotal role in enabling innovations across fields such as mobile communications,





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autonomous vehicles, IoT, and artificial intelligence. He emphasized the rising global demand for skilled professionals and the significant talent gap, particularly in India.

- Career Guidance: Drawing from his professional journey, Mr. Singh shared practical advice on enhancing employability through industry-recognized certifications, internships, and project-based learning. He urged students to remain curious and adaptive to technological progress.
- Interactive Discussions: The session was highly engaging, with Mr. Singh addressing student queries on technical skills, industry trends, and entrepreneurial opportunities in the semiconductor ecosystem. His personal anecdotes provided a relatable and motivational perspective.
- Encouragement for Innovation: He highlighted the importance of hands-on learning, entrepreneurship, and continuous skill up gradation, encouraging students to pursue both conventional roles and innovative startup ventures in the VLSI sector.

Closing Remarks

In his concluding address, Mr. Singh expressed optimism about India's emerging leadership in the global semiconductor ecosystem. He encouraged students to seize upcoming opportunities by building strong technical foundations and embracing lifelong learning.

This collaborative initiative represents a significant step in JUIT's continuous efforts to bridge the academia—industry gap, fostering skill development and technological innovation in semiconductor and electronics engineering.

About VLSI for All Pvt. Ltd.

VLSI for All Pvt. Ltd. is a leading organization dedicated to providing high-quality VLSI education and training. With a focus on bridging the industry skill gap, the company empowers students and professionals with the practical expertise required to excel in the semiconductor field.





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