



One Week **FACULTY DEVELOPMENT PROGRAM** **VLSI DESIGN: BRIDGING THEORY AND PRACTICE** **JUNE 09 - 13, 2025, OFFLINE MODE**

Registration Fees: Rs. 499/-
Accommodation & Meals included

Last Date of Registration: 31st May 2025

Link for Registration:

www.tinyurl.com/JUITVLSI

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Organizer

Department of Electronics & Communication Engineering, Jaypee University of Information Technology

in collaboration with

**Electronics & ICT Academy, Centre for Development of Advanced Computing (C-DAC), Mohali,
IEEE India Council, IEEE Delhi Section, IEEE MDC and WIE Delhi Section.**

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