

# Latest EDA Tools for Electronic Circuit Design: Xilinx Vivado, T-SPICE

## REPORT

Department of Electronics and Communication Engineering organized a 2-day workshop on “*Latest EDA Tools for Electronic Circuit Design: Xilinx Vivado, T-SPICE*”. The program was scheduled from October 31 - November 01, 2018 in CL1 at Jaypee University of Information Technology, Wagnaghat, Solan, H.P in collaboration with CoreEL Technologies, Delhi.

CoreEL is a leading systems design, FPGA Core and Design services company, located in Bangalore, India's Silicon Valley. They are a pioneer among the Indian companies that envisaged the concept of IP cores and made it a successful business proposition. CoreEL is associated with Xilinx as a distributor for over twenty years, from 1993 onwards and has the credit of popularizing Xilinx FPGA technology in India.

Dr. Shruti Jain and Dr. Harsh Sohal were the Convener of the program. The program was started with the welcome speech by Prof. M. J. Nigam, HOD, ECE. Thereafter Mr. Paramhans and Mr. Ankur discussed on various latest versions available in Xilinx and SPICE. First-day Hands-on session on Vivado was done and on the second-day, hands-on session on SPICE was conducted. More than 30 faculty members, staff & 15 Research scholars have participated in this workshop.

