Jaypee University of Information Technology

Department of Electronics & Communication Engineering

In collaboration with

Department of Technical Education , Vocational & Industrial Training, Government of Himachal Pradesh

organizing

One-week Hands-on Workshop on "VLSI Design"

(04th - 09th March, 2024)

Patron
Prof. R.K. Sharma, Vice Chancellor

Advisor
Prof. Ashok Kumar Gupta,
Dean (Academics & Research)

Program Chair
Prof. P.B. Barman, Head, PMS Department
Prof. Rajiv Kumar, Head, ECE Department

Finance Chair
Maj. Gen Rakesh Bassi (Retd.),
Registrar and Dean of Students

Convener
Prof. Shruti Jain, Associate Dean
(Innovation) and Prof. ECE

Coordinators
Dr. Harsh Sohal, Prof. (Asso.), ECE
Dr. Pardeep Garg, Prof (Asst.), ECE

Resource Persons

- Dr. Harpreet Singh Jatana, Former group head at SCL Mohali and ISRO.
- Dr. Balwinder Singh, Joint Director & Head, ACS Division, CDAC Mohali.
- Dr. Balwinder Raj, Professor (Asso), NIT Jalandhar.
- Dr. Satish Chandra Tiwari, DFT Manager (Principal Design Engineer), NXP Semiconductors.
- Mr. Mayank Singh, Verification Engineer, NXP Semiconductors.
- Mr. Ankur Sangal, Technical Lead, HCL Technologies.
- Mr. Anish K Sharma, Entuple Technologies Pvt. Ltd. Bangalore.

About Jaypee University of Information Technology

JUIT was conceived by a joint vision of the Govt. of Himachal Pradesh and the Founder Chairman of Jaypee Group Shri Jai Prakash Gaur Ji in 2000. Being the first state private university we have the honor to have Governor of H.P. being the Chancellor. The JUIT is also a member of the Association of Indian Universities (AIU). The academic activities of JUIT commenced in July 2002 and currently offer undergraduate B.Tech. and M.Tech. degree programs in Bioinformatics, Biotechnology, Civil Engineering, Computer Science & Engineering, Electronics & Communication Engineering, Electronics & Computer Engineering, Civil Engineering with specialization in Computer Science and Information Technology. The University is also offering B.Sc., M.Sc.,BBA and Ph.D. degree in different departments. The University campus is spread over 25 acres of lush green picturesque slopes of Waknaghat hills in the Solan District of Himachal Pradesh. The smart campus is pollution free and enjoys lovely weather throughout the year.

About Department of Technical Education, Vocational and Industrial Training

The Directorate of Technical Education, Vocational and Industrial Training, Himachal Pradesh at Sundernagar is having the Administrative Control of its functional units in the State. The Department is responsible for providing Technical Education, Vocational & Industrial Training in the State at various levels i.e. ITIs, diploma courses for middle level technicians, undergraduate/ postgraduate in Engineering and Technology/Pharmacy. The Directorate has two Wings. The Technical Education Wing the Department has been entrusted with the responsibility of managing Degree and Diploma level Technical Institutions in the State of Himachal Pradesh. The Industrial Training Wing of the Department has been entrusted with the responsibility of imparting training in Engineering and non-engineering trades to cater to the needs of the industry in respect of skilled workers. Similar to the National Council for Vocational Training at the Central level, the State Council for Vocational Training at the State level is responsible for coordinating the integrated development of Industrial Training. The main motto of the technical institution is to provide superior quality trained manpower, having social commitment along with career advancement to meet the challenges and opportunities in thrown up by the fast evolving society in the 21st Century.

Objective of the Workshop

Very Large Scale Integration (VLSI) design is an important field that deals with the design and fabrication of integrated circuits. The proposed workshop provides **B.Tech./ B.Sc./ diploma / M.Tech.** students with necessary skills and knowledge to work in the field of VLSI design. The main objective of this workshop is to deliver the design aspects and provide hands-on experience with VLSI design tools like Cadence and Vivado (Xilinx) etc. in the Digital and Analog design domain where participants will get an extensive knowledge of the ideas and concepts of VLSI designs along with knowledge of FPGA.

Workshop will be conducted in Offline mode that comprises expert talks followed by Hands-on sessions. For registering the workshop, participants need to pay Rs. 500/- (Five hundred rupees only) which includes fooding and lodging charges for H.P. Certificates will be given to the attendees.
For Registration, visit: http://tinyurl.com/juitecevlsi

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