

# **Workshop on Design and Implementation of Logics using FPGA**

**Organised by IEEE Student Branch of JUIT**

*Technically sponsored by ECE Department of JUIT*

**One Week Workshop (16<sup>th</sup> – 23<sup>rd</sup> Nov, 2015 – Excluding Sunday)**

**Venue: CL1, JUIT**

<b>Course Description</b>	This course provides introduction to Digital Designing with Reconfigurable VLSI Hardware using VLSI Tools
<b>Level</b>	Introductory
<b>Duration</b>	7 Days
<b>Who should attend?</b>	Students who are new to FPGAs and wish to use programmable devices in digital design
<b>Pre-requisites</b>	Digital design knowledge

**Instructor: Mr. M Wajid (Department of ECE, JUIT)**

## **Key Features:**

- Introduction to VLSI and VLSI Tools
- Reconfigurable VLSI Hardware
- Where can we use FPGA?
- FPGA Design Flow
- Implementing design on FPGA (Hands on Session)

**Seats are limited and selection will be based FCFS**

**Registration Fee: INR 500**