



# ISPPCC

ENABLING TECHNOLOGIES - BRIDGING THE GAP



IEEE  
COMMUNICATIONS  
SOCIETY

## JUIT SOLAN \* 26-28 SEPTEMBER 2013 \* CALL FOR PAPERS

[www.juit.ac.in/ispcc\\_2013](http://www.juit.ac.in/ispcc_2013)

2013 IEEE International conference on Signal Processing, Computing and Control (ISPPCC), will be organized by Jaypee University of Information Technology. The aim of ISPPCC is to serve as a forum for researchers, developers, practitioners, educators working in the areas of signal processing, computing, control and their applications to present current and future work as well as to exchange research ideas. 2013 ISPPCC invites authors to submit their original and unpublished work that demonstrates current research in all areas of Signal Processing, Communications, Computing, Control and their applications as well as proposals for tutorials.

### TOPICS OF INTEREST INCLUDE BUT ARE NOT LIMITED TO :

#### Signal Processing

- \* Audio and Acoustic Signal Processing
- \* Multimedia Signal Processing
- \* Bio Imaging and Signal Processing
- \* Magnetic Imaging
- \* Signal Processing Education
- \* Design and Implementation of Signal Processing Systems
- \* Image, Video and Multidimensional Signal Processing
- \* Signal Processing for Communications and Networking
- \* Machine Learning for Signal Processing
- \* Adaptive Signal Processing
- \* Fractional Transforms and Applications
- \* Speech and Language Processing

#### Network and Communication

- \* Ad-hoc and Wireless Sensor Networks
- \* Quality of Service
- \* Wireless Communications and Networks
- \* Microwave Theory and techniques
- \* Antenna Theory and Design
- \* Terahertz Communication Technologies
- \* Information and Coding Theory
- \* Communications and Information Security
- \* Telepresence and Teleportation
- \* Mission Critical Networking and its Applications
- \* Body Area Networks
- \* Cognitive Radio and Networks
- \* Power Line Communications
- \* Optical Networking
- \* Cloud Communications and Networking

#### VLSI and Embedded Computing

- \* Reconfigurable Computing/FPGA
- \* Network on Chip (NoC), System on Chip (SoC)
- \* Design, Modeling and Simulation of Digital/Analog IC
- \* VLSI Circuits and Systems
- \* CAD for VLSI
- \* Interconnect Modeling and Algorithms
- \* Micro-Electro-Mechanical-Systems (MEMS) Mixed Analog/Digital Systems

#### Control and Automation

- \* Instrumentation and Automation
- \* Robotics
- \* Robust, Adaptive and Resilient Control Systems
- \* control in Business, Management and Economics
- \* Process Control, Failure Analysis, yield Enhancements and Modeling
- \* Networked Control System
- \* Fault Tolerant Control System
- \* System Performance and Reliability

#### Artificial Intelligence and Neural Networks

- \* Artificial Intelligence Algorithms
- \* Fuzzy and Neural Systems
- \* Artificial Intelligence Tools and Applications
- \* Bioinformatics
- \* Pattern Recognition
- \* Natural Language Processing
- \* Knowledge Based Systems
- \* Parallel Processing

### IMPORTANT DEADLINES

Submission of Manuscript - **April 15, 2013.**      Camera-ready Manuscript      - **July 6, 2013.**  
Acceptance Notification      - **June 15, 2013.**      Registration Deadline      - **July 5, 2013.**

General Chair - Prof. S.V. Bhooshan  
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