

Patrons

Prof. Dr. Vinod Kumar

Vice-Chancellor, JUIT, Waknaghat

Prof. Dr. Samir Dev Gupta

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HoD, ECE*

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&

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Electronics & Communication Engineering Department

The Department offers a four-year undergraduate program (B. Tech) and a two-year postgraduate programme (M. Tech) in Electronics and Communication Engineering. The Department also runs an active doc-oral programme. The courses offered cover areas related to Signal Processing, VLSI Circuit, Embedded Systems, Microwave Engineering and Communication Systems.

The Department provides research facilities including a library with the latest journals to its faculty members & interested students and encourages them to do research in the upcoming areas. The department also has research collaboration with universities abroad and has joint research projects.

Registration

Participants can register by sending the duly filled registration form along with registration fee. Fees can be submitted online.

Registration (without Accommodation, lunch only) Rs 1000/-

Registration (with Accommodation and all meals) Rs 2500/-

Meenakshi Sood & Shruti Jain

Department of ECE,

Jaypee University of Information Technology

Waknaghat, Solan, Himachal Pradesh-173234



FACULTY DEVELOPMENT PROGRAM

on

EMERGING TRENDS IN VLSI AND COMMUNICATION (ETVC-18)

09 – 18 June, 2018

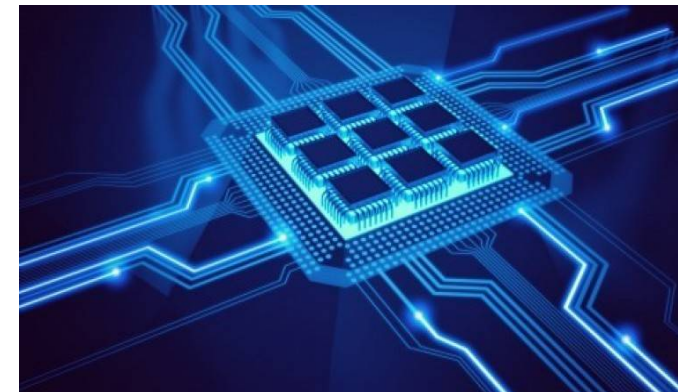
Organized by:

Dept. of Electronics & Communication Engineering

Jaypee University of Information Technology

Waknaghat, Solan, HP 173234

www.juit.ac.in



External Resource Persons

Prof. Dinesh Kant

RMIT, University, Melbourne, Australia.

Professor Dinesh Kumar received his Bachelor of Technology in Electrical Engineering from IIT, Madras (Chennai) and Doctor of Philosophy from IIT, Delhi. He is working as a Professor in Electrical and Computer Engineering, at RMIT University, Melbourne city Australia. Dinesh has received over \$4 million in research funds over the past 12 years in research funding.



Dr. Shubhajit Roy Chowdhury

IIT Mandi, India.

Dr. Shubhajit Roy Chowdhury did his M.Tech and PhD from the Jadavpur University. He was gold medalist in M.Tech as well as B.Tech program. His research interest includes VLSI Architectures, Biomedical Embedded Systems, Non invasive diagnostic systems etc. Prior to joining IIT Mandi, he was associated with IIIT Hyderabad, Indian Statistical Institute, Kolkata, and Jadavpur University. Elected as a member of Award Committee for Gandhian Young Technological Innovation (GYTI) Award by the Society for Research and Initiatives for Sustainable Technologies and Institutions (SRISTI) in the area of Health Care devices in the year 2018.



Prof. (Dr.) Vinod Kumar

Vice Chancellor, JUITW

Dr. Vinod Kumar was Deputy Director and Professor of Electrical Engineering at IIT Roorkee. He received his B.Tech degree from Punjab Engineering College, Chandigarh and M.Tech. & Ph.D. from IIT Roorkee. He has many academic awards, distinctions and scholarships to his credit including the Khosla Research Awards of University of Roorkee, Ist IETE-R S Khandpur Medical Instrumentation Gold Medal in recognition of life time achievements and outstanding contribution in the field of Bio-medical Instrumentation 2006.



CoreEL Technologies



CoreEL is a leading systems design, FPGA Core and Design services company, located in Bangalore, India's Silicon Valley. We are a pioneer among the Indian companies that envisaged the

concept of IP cores and made it a successful business proposition. CoreEL is associated with Xilinx as a distributor for over twenty years, from 1993 onwards and has the credit of popularizing Xilinx FPGA technology in India. Name CoreEL stands for Core Electronics and that is our strength. We are a Customer Application Specific Product & Solutions (CASPS) company offering innovative solutions, ranging across Intellectual Property (IP) cores, Design & Development, -System Design & Prototype Development, Next-Gen Digital products, integrated solutions, Low Volume Manufacturing, System Upgrades and Obsolescence management, EDA tools, COTS products, Semiconductor solutions and Technology Training.

Laboratory Virtual Instrument Engineering-Workbench (LabVIEW)



LABVIEW is a system-design platform and development environment for a visual programming language from National Instruments. The graphical language is named "G"; not to be confused with G-code. Originally released for the Apple Macintosh in 1986, LabVIEW is commonly used for data acquisition,

instrument control, and industrial automation on a variety of operating systems (OSs), including Microsoft Windows, various versions of Unix, Linux, and macOS.

AdvanceTech India Pvt Ltd

AdvanceTech India Pvt Ltd, Zirakpur which deals in the field of Hardware Development, Embedded Products Development, Security & Surveillance and Engineers Training Programs. AdvanceTech India Pvt. Ltd. an Embedded Design House. AdvanceTech

India Pvt. Ltd. offers various Technical Education solutions, Products & Development tools to Engineering colleges, Universities, research organizations. We also offer the business of providing Technical education solutions & development tools to the educational & industrial customers.



SCHEDULE

Registration Begins: 9:00 am (June 9th 2018)

Date	Time	Events
09-06-2018	09:30AM	Inaugural function
	10:00 AM	Expert Lectures by Dr. Shubhajit Roy Choudhary, IIT Mandi
11-06-2018	10:00AM-4:00PM	Computational Paradigms - NI Lab View
12-06-2018	10:00AM-4:00PM	Xilinx Vivado Tool Flow with FPGA based coding techniques Lecture by Mr. Ankur Sangal, Sr. Application Engineer, and Mr. Paramahans Singh, Sr. Executive, CoreEL Technologies
13-06-2018	10:00AM-4:00PM	IP Integrator using Xilinx Vivado Tool Lecture by Mr. Ankur Sangal, Sr. Application Engineer, and Mr. Paramahans Singh, Sr. Executive, CoreEL Technologies
14-06-2018	10:00AM-4:00PM	Changing Technological Paradigms: 3G, 4G, GSM and Wireless Communication
15-06-2018	10:00AM-4:00PM	Recent trends in IoT and Antenna
16-06-2018	09:30AM	Expert Lecture by Prof. Dinesh Kant, Professor, Electrical and Biomedical Engineering, RMIT University, Australia
18-06-2018	10:00AM	Expert Lectures by Prof. Vinod Kumar, JUIT Solan
	11:00 AM	Programming Paradigm on Optimization Tools
	1:00 PM	Valedictory